PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Martin Hausner

Serial No.:

Unknown (National Phase of International Application

PCT/EP03/09052)

Filed:

February 11, 2005 (Priority Date Claimed August 14,

2002)

Examiner:

Unknown

Group Art Unit: Confirmation No.:

Unknown

Title:

Unknown

METHOD FOR SELECTIVELY REMOVING MATERIAL

FROM THE SURFACE OF A SUBSTRATE, MASKING

MATERIAL FOR A WAFER, AND WAFER WITH MASKING

MATERIAL

Our Ref. No.:

BEET-09

Cincinnati, Ohio 45202

February 11, 2005

Mail Stop PCT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT

In accordance with the duty of candor and good faith imposed by 37 C.F.R. §1.56 and means of complying therewith according to 37 C.F.R. §§1.97 and 1.98, the references listed on the attached Form PTO-1449 are called to the

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attention of the United States Patent and Trademark Office in connection with the above-identified patent application. Because the requirement (37 C.F.R.

§1.98(a)(2)(i)) for submitting copies of U.S. patents and published applications has been waived, copies of only the foreign cited references and/or other documents are enclosed herewith.

No representation is made that the cited art is the only art or that the cited art represents the best art.

The Examiner is urged to consider all of the cited documents and to make an independent evaluation of the teachings and materiality of each.

No fees are believed to be due. However, if any additional fees are necessary to complete this communication, please apply them to Deposit Account No. 23-3000.

Respectfully submitted,

WOOD, HERRON & EVANS, L.L.P.

David H Brinkman

Reg. No. 40,532

2700 Carew Tower 441 Vine Street Cincinnati, OH 45202 (513) 241-2324 - Voice (513) 421-7269 - Facsimile

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SUBSTITUTE FORM PTO-1449 (MODIFIED)



U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE ATTY! KET NO. BEET/09

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use several sheets if necessary)

APPLICANT Martin Hausner

FILING DATE

February 11, 2005

GROUP Unknown

(37 CFR 1.98(b))

U.S. PATENT DOCUMENTS

EXAMINER INITIAL			PA	TEN	1 TV	IUM	BE	₹	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
	A.A	6	0	ż	4	9	4	6	06/2000	Ouellet et al.	438	689	02/1997	
	A.B	6	4	5	8	6	1	5	10/2002	Fedder et al.	438	50	09/1999	
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FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS.

		DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION (YES/NO)
	A.L	DE 196 41 288 A1	04/1998	Germany	C23F	4/00	No
	A.M	DE 197 36 370 C2	12/2001	Germany	H01L	21/3065	No
·	A.N	DE 199 00 179 C1	02/2000	Germany	C23F	4/00	No
	A.O	DE 199 27 806 A1	01/2001	Germany	H01L	21/3065	No
	A.P	DE 44 20 962 C2	09/1998	Germany	H01L	21/308	No
	A.Q	JP 05021395 A	01/1993	Japan	H01L	21/302	Abstract Only

OTHER DOCUMENTS (Including Author, Title, Date, Place of Publication)												
A.R	Ayon et al., Anisotropic silicon trenches 300-500 um deep employing time multiplexed deep etching (TMDE), Sensors and Actuators A, Vol. 91, 2001, pp. 381-385											
A.S	Database WPI, A method of through-etching substrate is provided to efficiently perform a cooling processing of a substrate and to improve a profile around a penetrated hole by using a metal material, Section Ch, Week 200278, Derwent Publications Ltd., London, GB, Class L03, AN 2002-720751 (1 page)											
A.T	Dijkstra et al., Can SEA (Semiconductor Equipment Assessment) also deliver for MEMS?, Horological Journal, Ashford, GB, Vol. 556, No. 302, 2001, pp. 684-690											

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APPLICANT Martin Hausner FILING DATE

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		PATENT NUMBER							ISSUE DATE	PATENTEE			CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
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B.L	JP 05302182 A	11/1993	Japan	C23F	4/00	Abstract Only
B.M	JP 07130751 A	05/1995	Japan	H01L	21/3213	Abstract Only
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В.Р	WO 01/O6539 A1	01/2001	WIPO	H01J	37/32	No
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OTHER DOCUMENTS (Including Author, Title, Date, Place of Publication)

B.R	IBM Corp., IBM Technical Disclosure Bulletin, Vol. 34, No. 5, October 1991, pp. 368-370
 B.S	EPO, International Search Report, PCT/EP03/09052, May 18, 2004 (14 pages)
В.Т	Kong et al., Reactive Ion Etching of SiC Using C2F6/02 Inductively Coupled Plasma, Journal of Electronic Materials, Vol. 31, No. 3, March 2002, pp. 209-213

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C.R	A. J. Perry and R. W. Boswell, <u>Fast anisotropic etching of silicon in an inductively coupled plasma reactor</u> , Applied Physics Letters, American Institute of Physics, New York, Vol. 55, No. 2, July 10, 1989, pp. 148-150
C.S	Rakhshandehroo et al., <u>Dry etching of Si field emitters and high aspect ratio resonators using an inductively coupled plasma source</u> , Journal of Vacuum Science and Technology: Part B, American Institute of Physics, New York, Vol. 16, No. 5, Sept./Oct. 1998, pp. 2849-2854
С.Т	Song et al., Method Of Through-Etching Substrate, United States Patent Application Publication No. 2003/0162402A1, Publication Date of August 28, 2003 (14 pages)

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EXAMINER: Initial if citation considered, whether or not in conformance. Draw line through citation only if not in conformance and not considered. Include a copy of this form with next communication to applicant.

Jason W. Weigold, Design and Fabrication of Submicrometer, Single Crystal Si Accelerometer, Journal of Microelectromechanical Systems, IEEE Inc., New York, Vol. 10, No. 4, December 2001, pp. 518-524

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Substitute Disclosure Form (PTO-1449)

SUBSTITUTE FORM PTO-1449 (MODIFIED)



U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

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EXPRESS MAIL NO. EV425514585US

APPLICANT Martin Hausner

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FILING DATE	
February 11, 2005	

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